

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	PROTOTYPES	04-April-2002	
B	PRODUCTION RELEASE	07-June-2002	
C	UPDATE PER SPRS174G	20-Jan-2003	

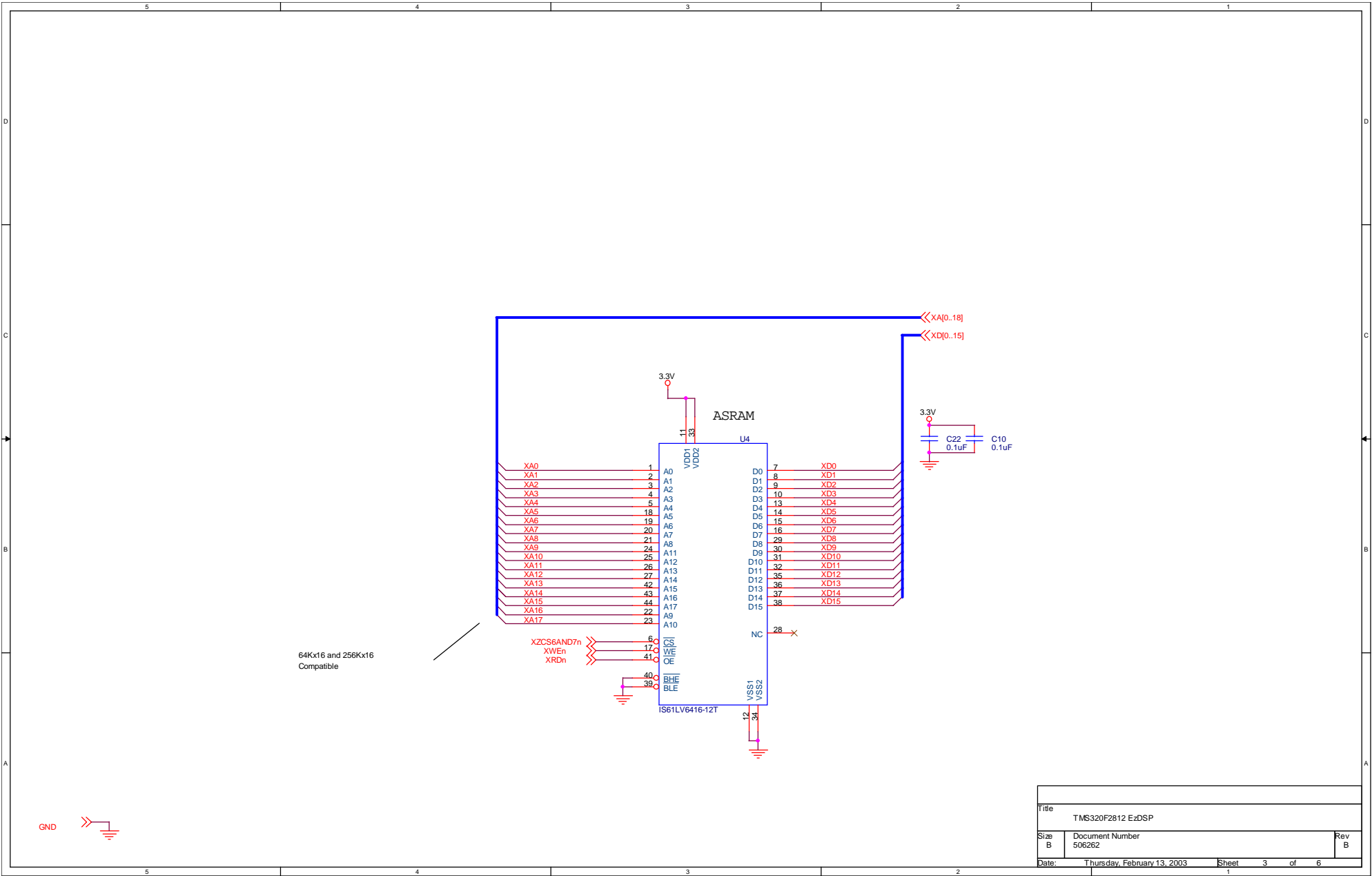
The TMS320F2812 EzDSP design is based on preliminary information (SPRS174G) for the TMS320F2812 device. This schematic is subject to change without notification. Spectrum Digital Inc. assumes no liability for applications assistance, customer product design or infringement of patents described herein.

REVISION STATUS OF SHEETS							APP	DATE
REV							CHK	DATE
SH							ENGR	DATE
REV							ENGR-MGR	DATE
SH							QA	DATE
REV	C	C	B	C	C	C	NEXT ASSY	USED ON
SH	1	2	3	4	5	6	APP	DATE

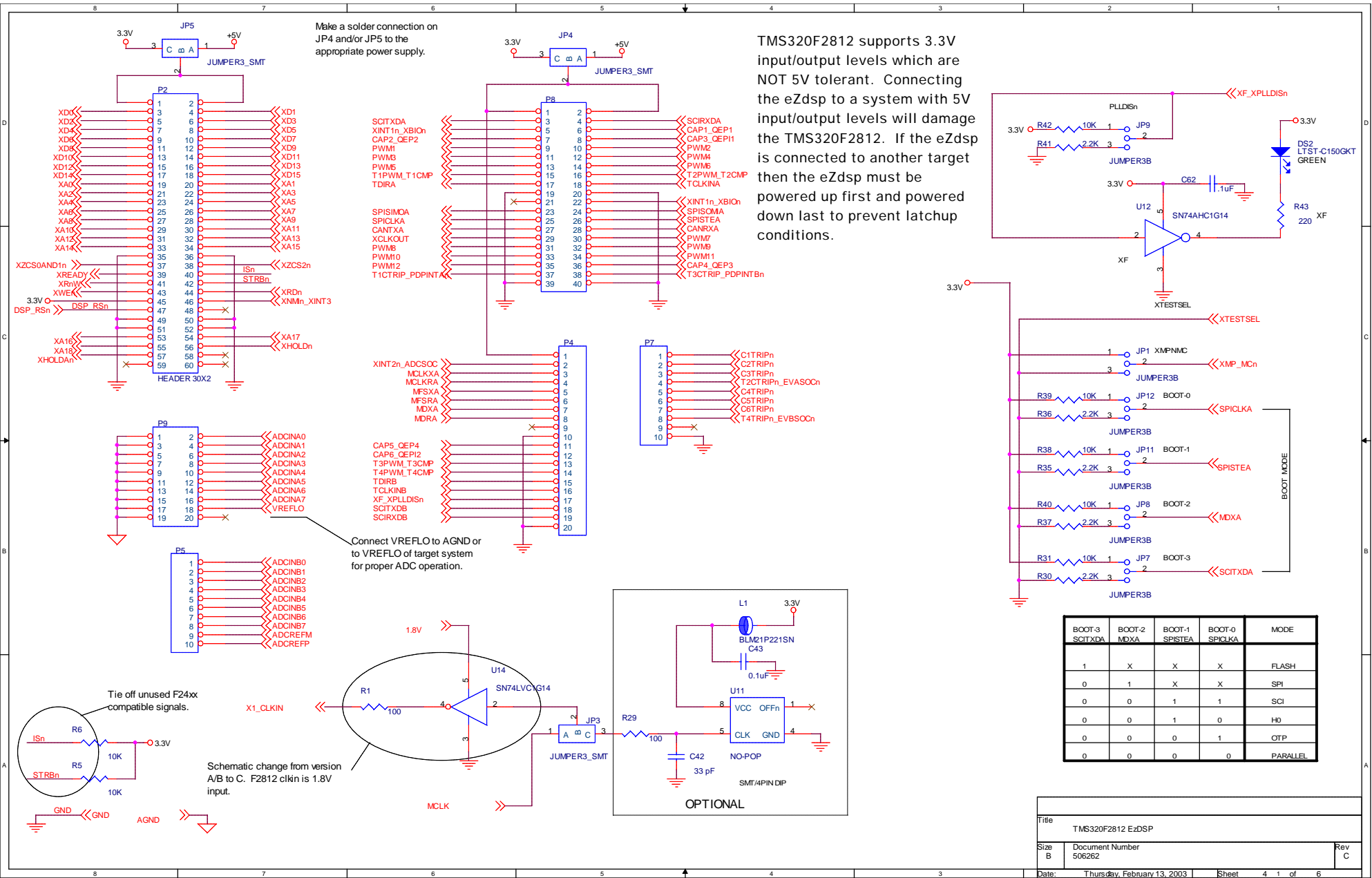
**SPECTRUM DIGITAL**

Title		
TMS320F2812 EzDSP		
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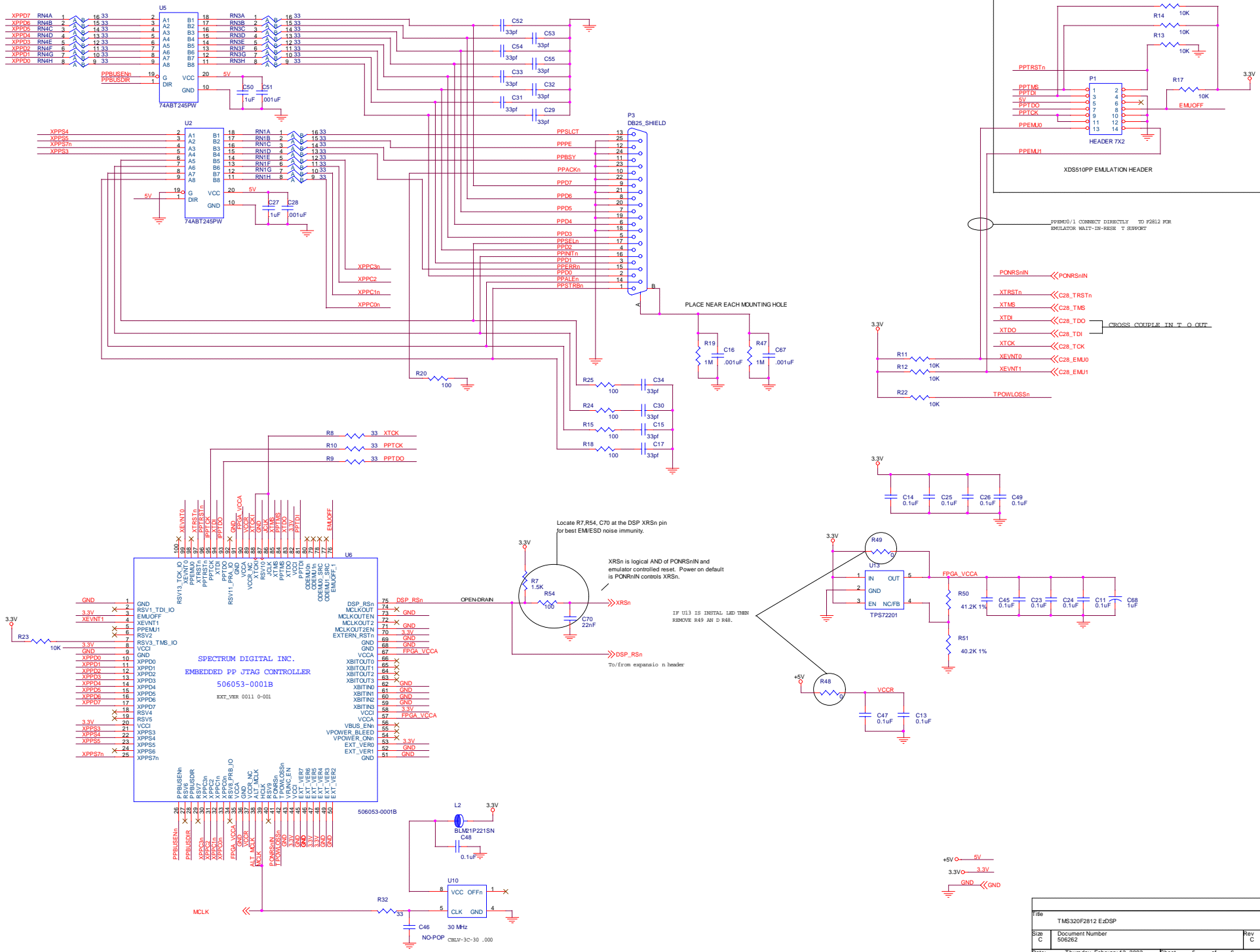




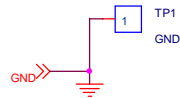
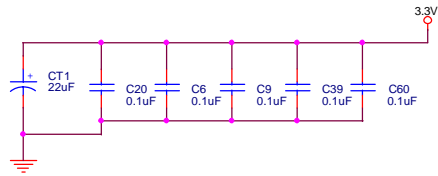
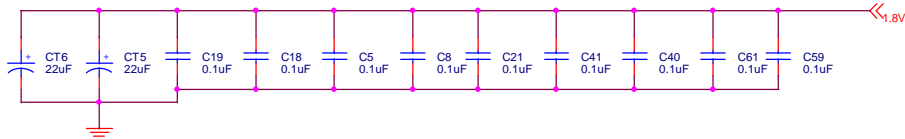
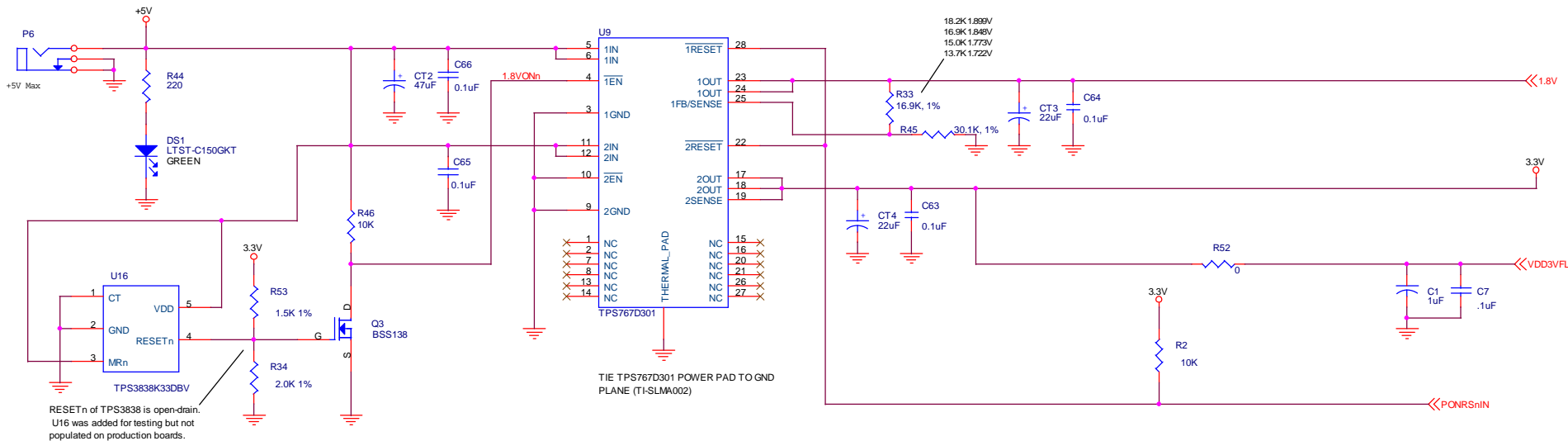
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BOOT-3 SCITXDA	BOOT-2 MDXA	BOOT-1 SPISTEA	BOOT-0 SPICKKA	MODE
1	X	X	X	FLASH
0	1	X	X	SPI
0	0	1	1	SCI
0	0	1	0	HD
0	0	0	1	OTP
0	0	0	0	PARALLEL



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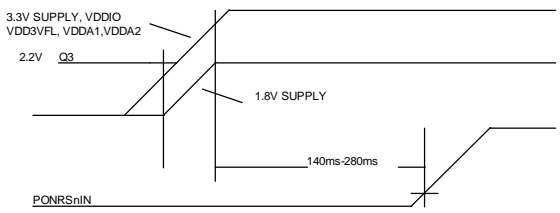
**POWER ON SEQUENCE NOTES**

3.3V THEN 1.8V

PONRSnIN, GOES HIGH > 200ms AFTER ALL POWER IS STABLE.

SET Q3 TO TURN ON WHEN 3.3V SUPPLY IS GREATER THEN 2.2 VOLTS. REGULATOR TURN ON DELAY AND RAMP RATE WILL ENSURE THAT 3.3V SUPPLY IS AT 2.5 VOLTS OR HIGHER BEFORE THE 1.8V SUPPLY REACHES 0.3 VOLTS.

U16 WILL TURN OFF THE 1.8V SUPPLY WHEN INPUT POWER FALLS BELOW 2.94V. THIS SPEEDS UP THE 1.8V SUPPLY TURN OFF.



**TPS767D301 RATINGS**

MAX Iout PER CHANNEL IS 1A

TPS767D301 PD = (Vin-Vout)\*Iout

VOLTAGE SUPPLY	MAX CURRENT	TPS767D301 PD
1.8V	333mA	1065mW
3.3V	307mA	521mW
		1586mW Total

PD(max) = (Tj(max) - Ta)/Rja

= (125 - 35)/27.9

= 3255mW