

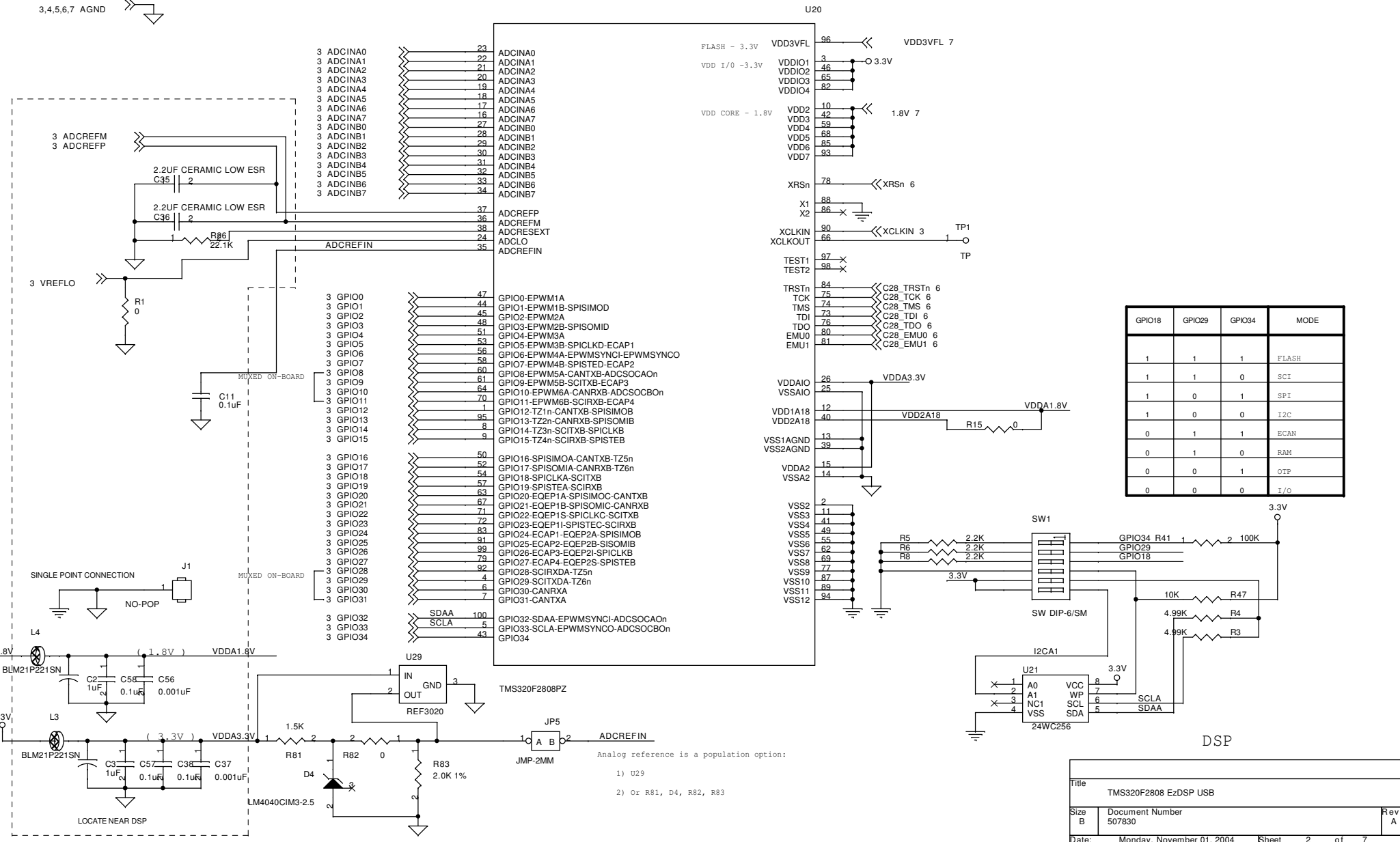
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	PROTOTYPES		

The TMS320F2808 EzDSP design is based on preliminary information (SPRS230A) for the TMS320F2808 device. This schematic is subject to change without notification. Spectrum Digital Inc. assumes no liability for applications assistance, customer product design or infringement of patents described herein.

SPECTRUM DIGITAL

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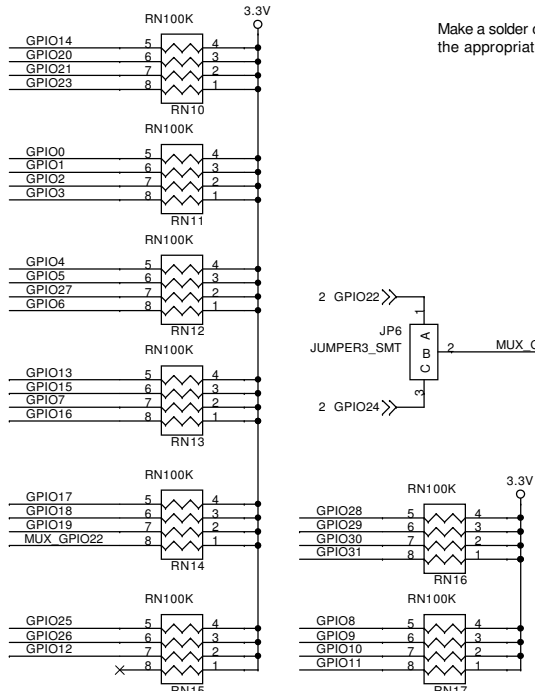
REVISION STATUS OF SHEETS								OWN	DATE		
REV								CHR	DATE		
SH								ENGR	DATE		
REV								ENGR-MGR	DATE		
SH								QA	DATE		
REV	A	A	A	A	A	A	A	NEXT ASSY	USED ON	MFG	DATE
SH	1	2	3	4	5	6	7	APPLICATION		RLSE	DATE



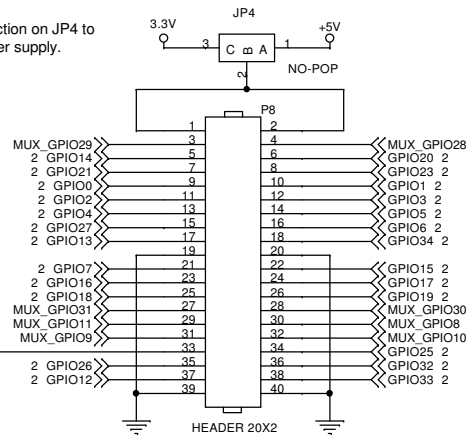
GPIO18	GPIO29	GPIO34	MODE
1	1	1	FLASH
1	1	0	SCI
1	0	1	SPI
1	0	0	I2C
0	1	1	ECAN
0	1	0	RAM
0	0	1	OTP
0	0	0	I/O

Analog reference is a population option:
 1) U29
 2) Or R81, D4, R82, R83

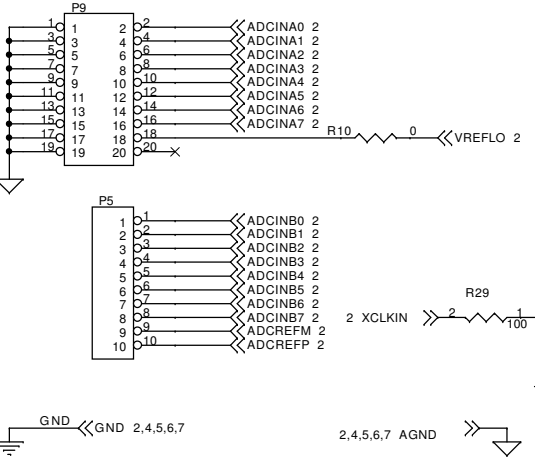
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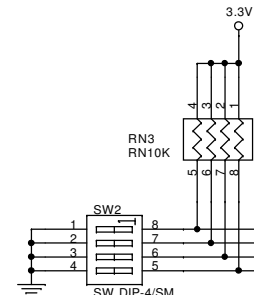
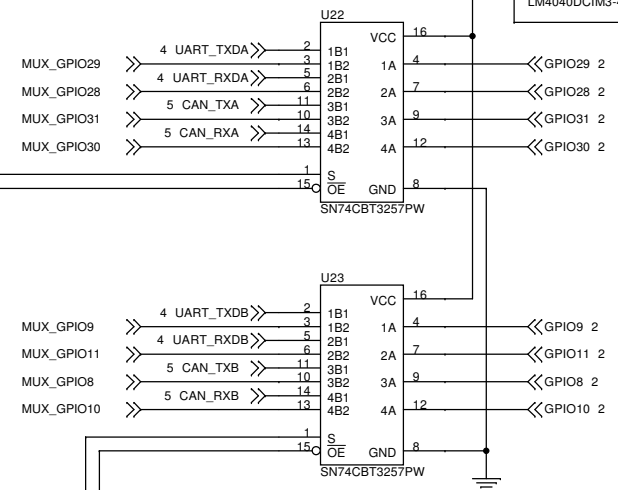
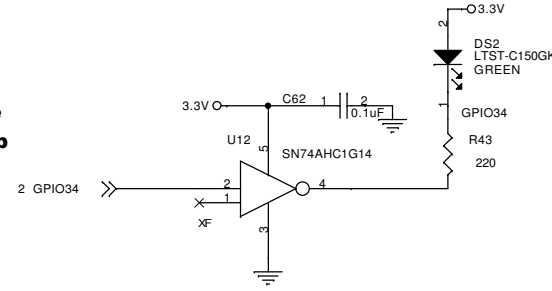
Make a solder connection on JP4 to the appropriate power supply.



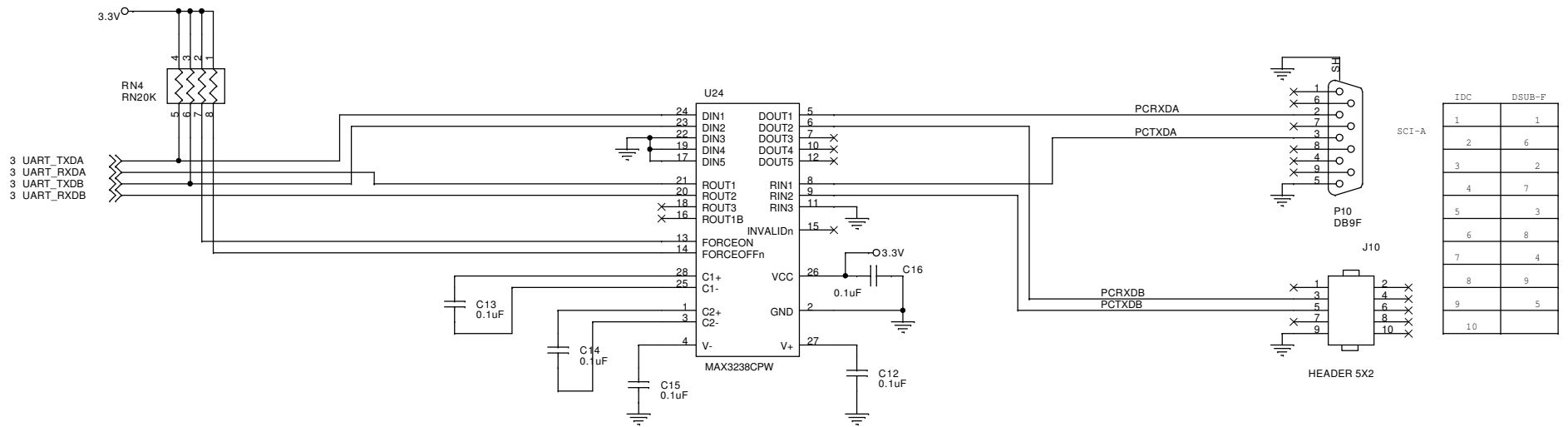
Pullups on all F2808 GPIO pins for backward compatibility with previous eZdsp and target boards.



TMS320F2808 supports 3.3V input/output levels which are NOT 5V tolerant. Connecting the eZdsp to a system with 5V input/output levels will damage the TMS320F2808. If the eZdsp is connected to another target then the eZdsp must be powered up first and powered down last to prevent latchup conditions.



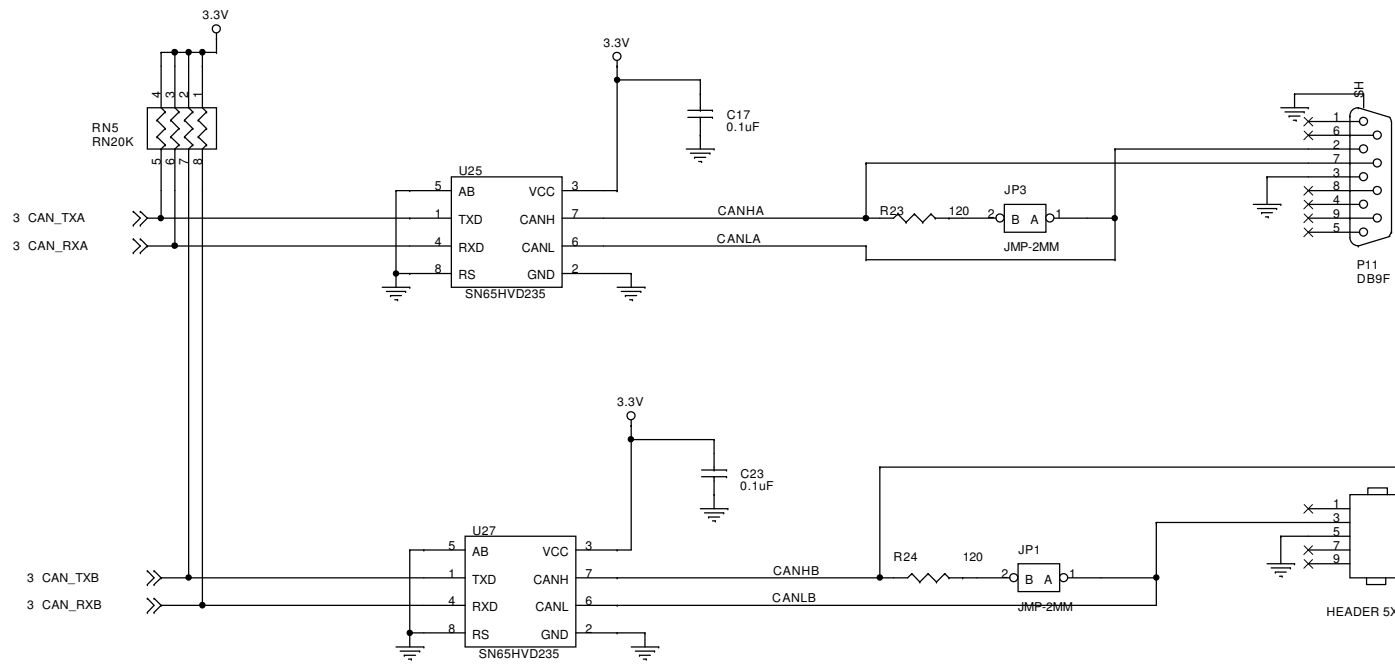
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IDC	DSUB-F
1	1
2	6
3	2
4	7
5	3
6	8
7	4
8	9
9	5
10	

GND <<< GND 2,3,5,6,7

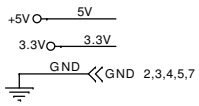
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IDC	DSUB-F
1	1
2	6
3	2
4	7
5	3
6	8
7	4
8	9
9	5
10	

GND << GND 2,3,4,6,7

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Locate R7, R54, C70 at the DSP XRSn pin for best EMI/ESD noise immunity.

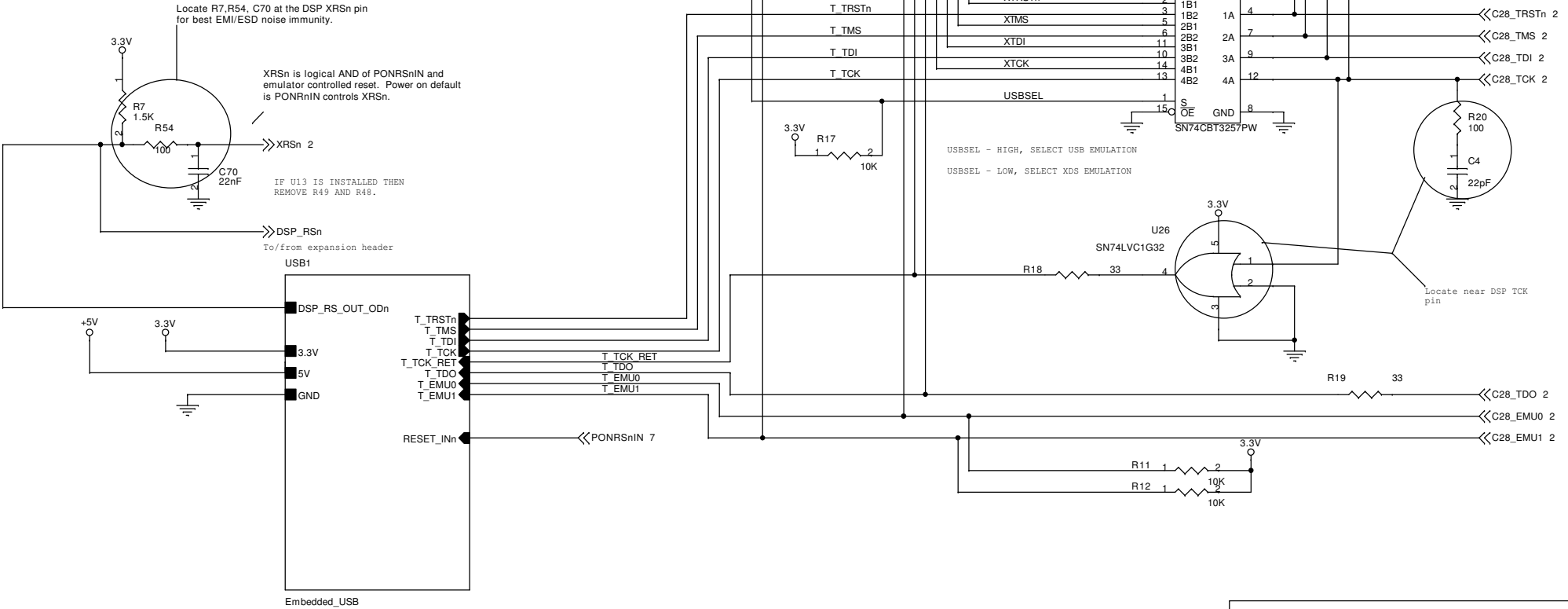
XRSn is logical AND of PONRSnIN and emulator controlled reset. Power on default is PONRnIN controls XRSn.

IF U13 IS INSTALLED THEN REMOVE R49 AND R48.

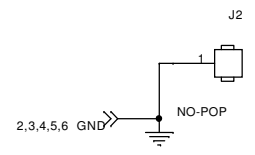
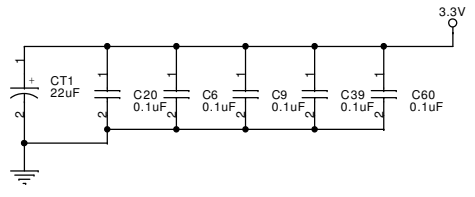
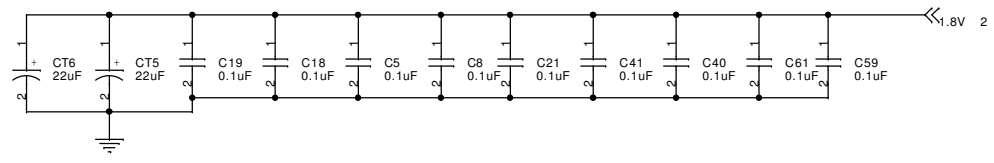
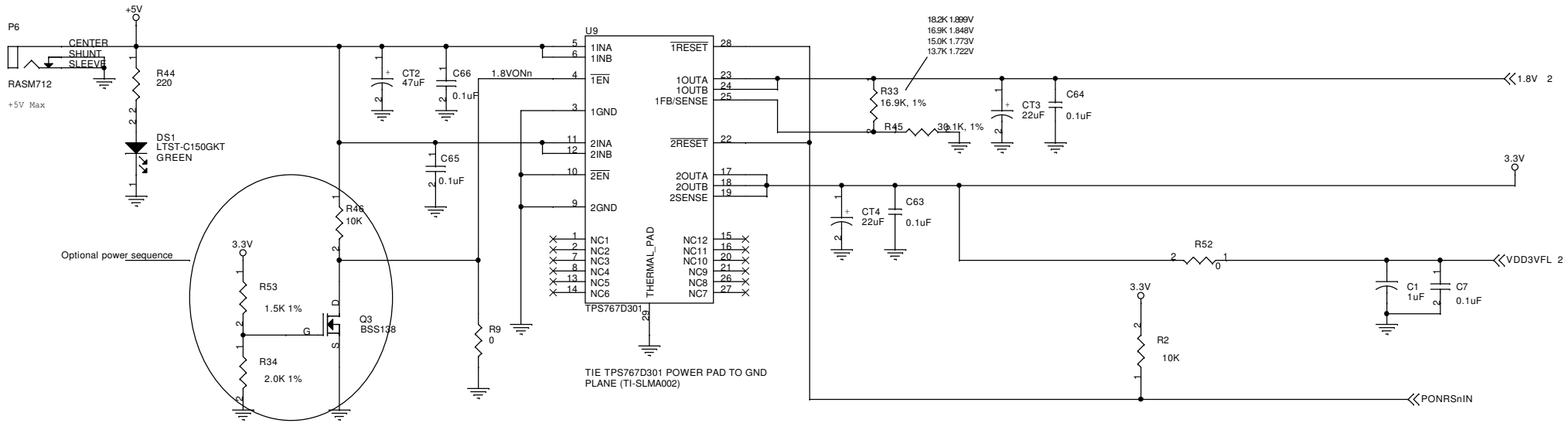
To/from expansion header

USB1

Embedded_USB



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